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03500.017602.

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
: Examiner: David J. Huisman
ATSUSHI DATE)
: Group Art Unit: 2183
Application No.: 10/671,785)
:
Filed: September 29, 2003)
:
For: PROCESSOR SYSTEM WITH)
EXCLUSIVE ACCESS TO ON-CHIP)
MEMORY BY ONE OF A BUILT-IN)
PROCESSOR OR AN EXTERNAL)
PROCESSOR BY USING A RESET)
STATE OF THE EXCLUDED)
PROCESSOR (AS AMENDED)) July 23, 2009

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

Sir:

In response to the Office Action dated April 23, 2009, please amend the
above-identified application, as follows:

I hereby certify that this correspondence is being transmitted via facsimile
to the U.S. Patent and Trademark Office at (571) 273-8300, on

July 23, 2009
(Date of Transmission)

Michael K. O'Neill (Reg. No. 32,622)
(Name of Attorney for Applicants)


Signature

July 23, 2009
Date of Signature